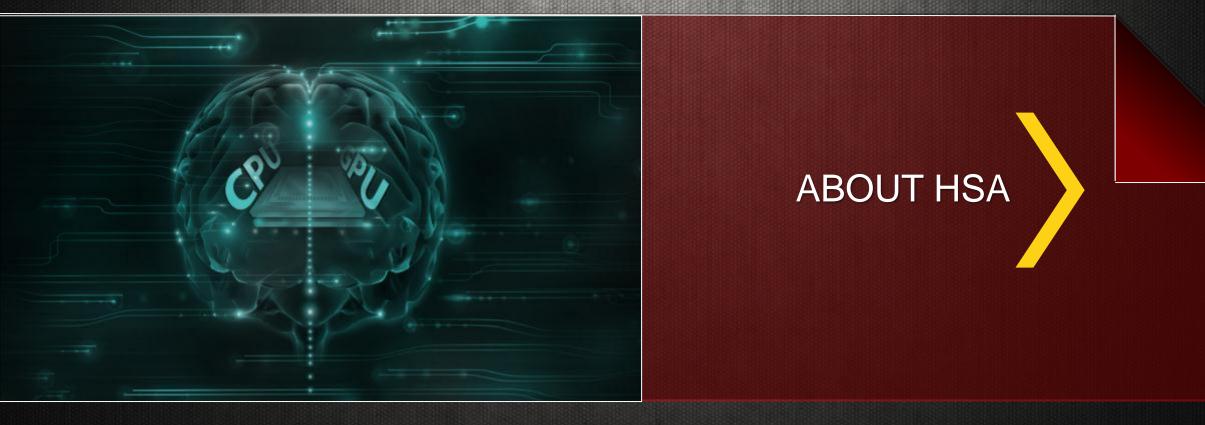
# AMDA

#### **AMD** heterogeneous Uniform Memory Access

PHIL ROGERS, CORPORATE FELLOW JOE MACRI, CORPORATE VICE PRESIDENT & PRODUCT CTO SASA MARINKOVIC, SENIOR MANAGER, PRODUCT MARKETING

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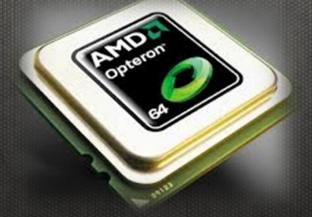
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#### 10 YEARS AGO...

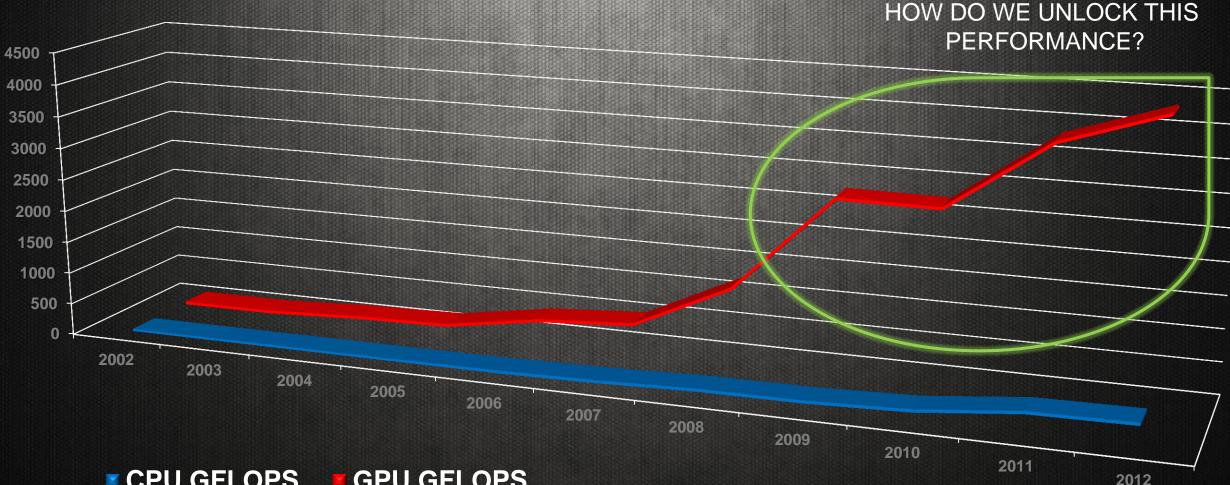
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## GPU COMPUTE CAPABILITY IS MORE THAN





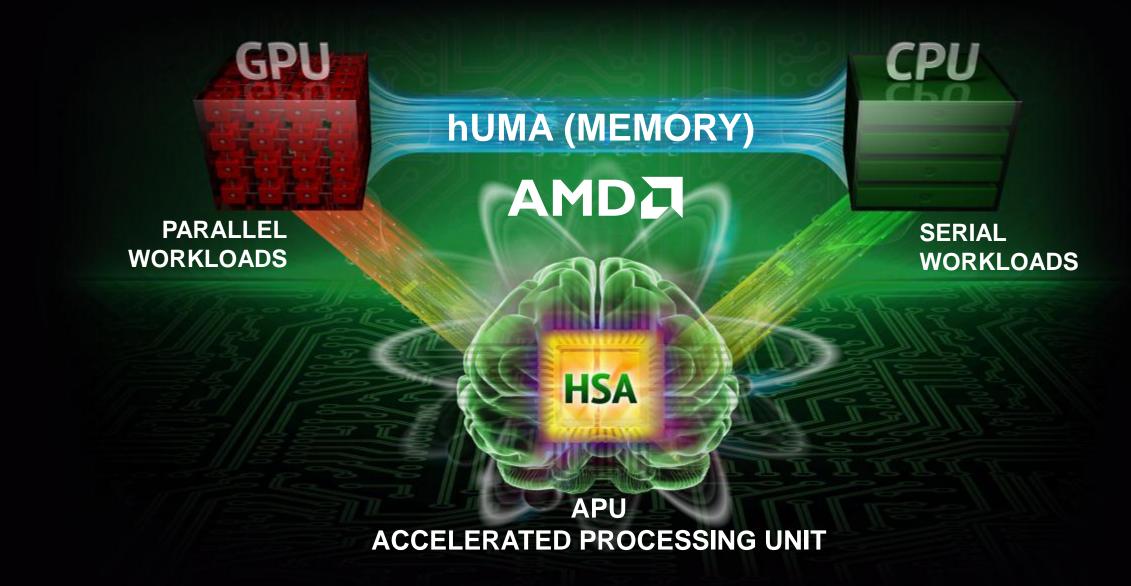
#### **CPU GFLOPS** GPU GFLOPS

See slide 24 for details

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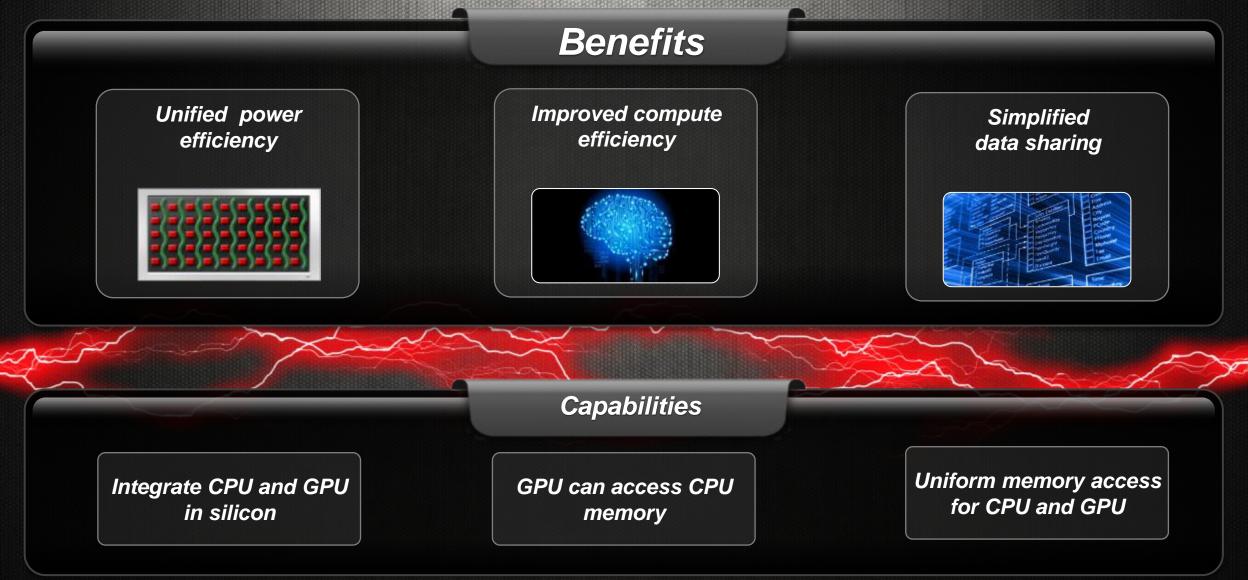
#### WHAT IS HSA?

An *intelligent computing architecture* that enables CPU, GPU and other processors to work in *harmony* on a single piece of silicon by *seamlessly* moving the right tasks to the best suited processing element



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#### **HSA EVOLUTION**



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#### WHAT IS hUMA?

heterogeneous UNIFORM MEMORY ACCESS

#### **UNDERSTANDING UMA**



#### Original meaning of UMA is Uniform Memory Access

- Refers to how processing cores in a system view and access memory
- All processing cores in a true UMA system share a single memory address space

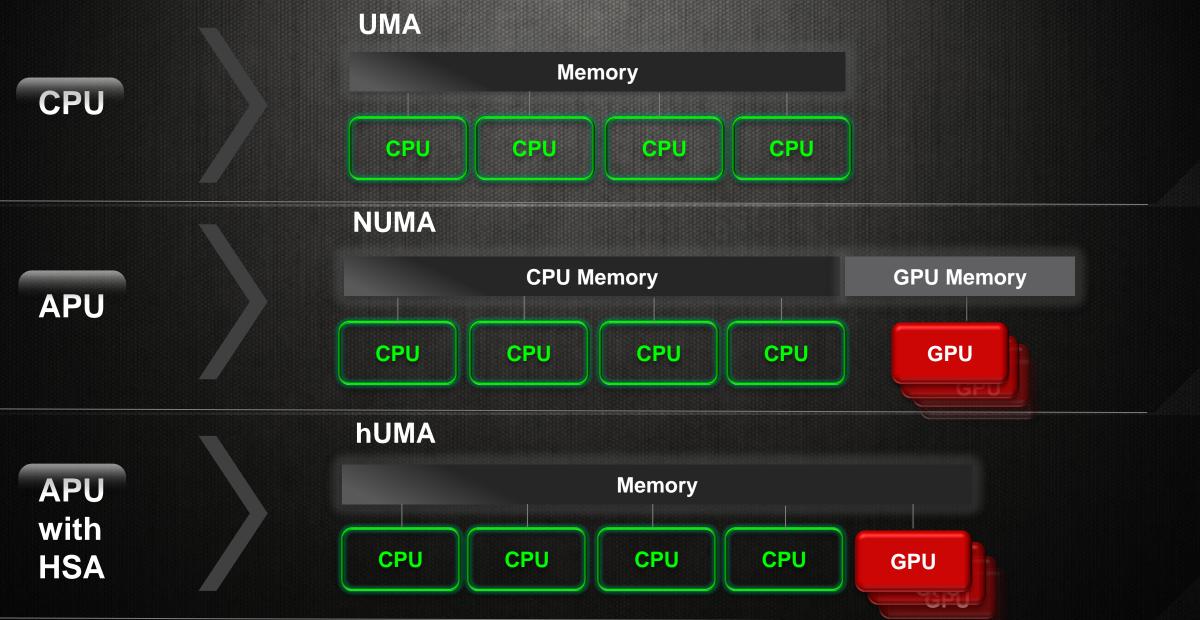
Introduction of GPU compute created systems with Non-Uniform Memory Access (NUMA)

- Require data to be managed across multiple heaps with different address spaces
- Add programming complexity due to frequent copies, synchronization, and address translation

HSA restores the GPU to Uniform memory Access

• Heterogeneous computing replaces GPU Computing

#### INTRODUCING hUMA



#### **hUMA KEY FEATURES**

#### **BI-DIRECTIONAL COHERENT MEMORY**

Any updates made by one processing element will be seen by all other processing elements -GPU or CPU

#### **PAGEABLE MEMORY**

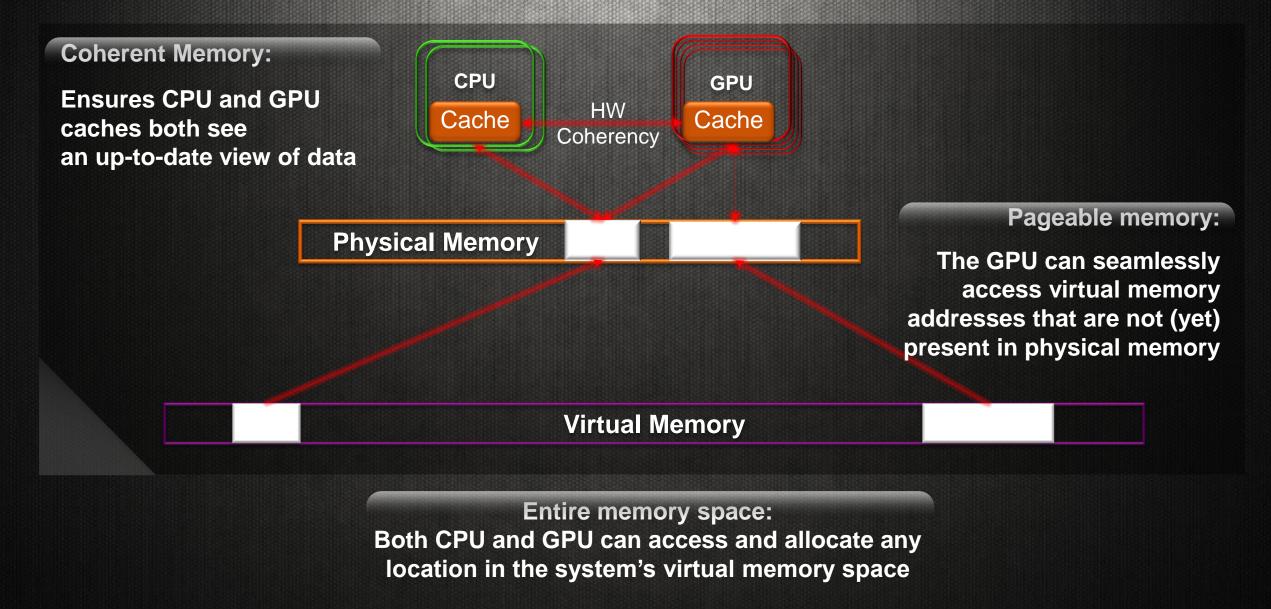
GPU can take page faults, and is no longer restricted to page locked memory

#### **ENTIRE MEMORY SPACE**

CPU and GPU processes can dynamically allocate memory from the entire memory space

#### **hUMA KEY FEATURES**

#### 

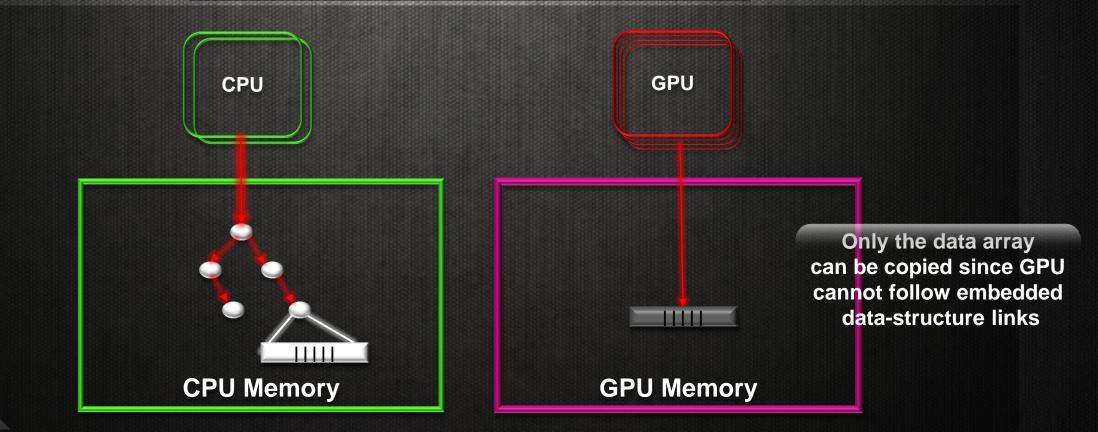


#### WITHOUT POINTERS\* AND DATA SHARING

#### 

#### Without hUMA:

- CPU explicitly copies data to GPU memory
- GPU completes computation
- CPU explicitly copies result back to CPU memory



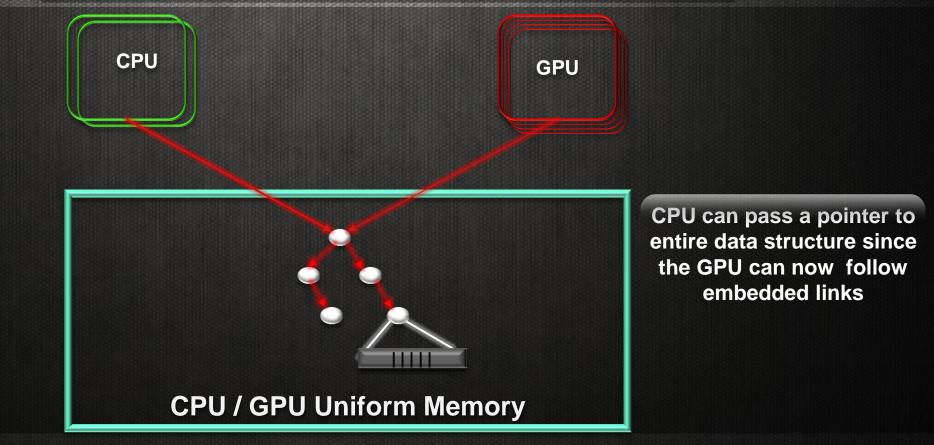
\*A Pointer is a named variable that holds a memory address. It makes it easy to reference data or code segments by a name and eliminates the need for the developer to know the actual address in memory. Pointers can be manipulated by the same expressions used to operate on any other variable

#### WITH POINTERS\* AND DATA SHARING



#### With hUMA:

- CPU simply passes a pointer to GPU
- GPU completes computation
- CPU can read the result directly no copying needed!



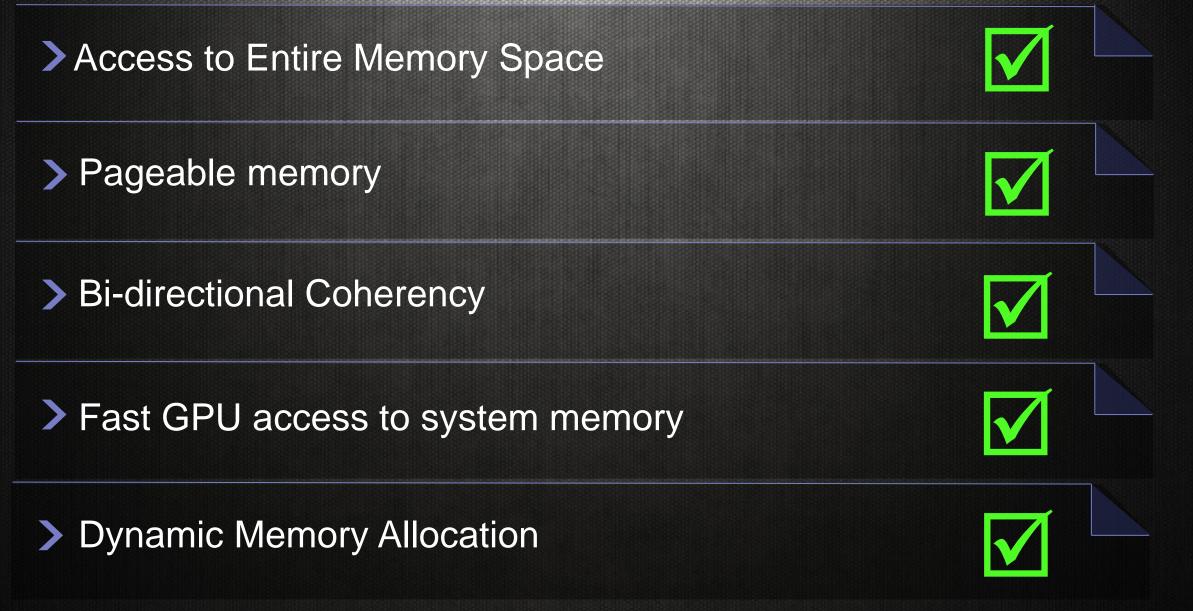
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#### TOP 10 REASONS TO GO FULLY HARDWARE COHERENT ON GPU/APU

- . Much easier for programmers
- 2. No need for special APIs
- 3. Move CPU multi-core algorithms to the GPU without recoding for absence of coherency
- 4. Allow finer grained data sharing than software coherency
- 5. Implement coherency once in hardware, rather than N times in different software stacks
- 6. Prevent hard to debug errors in application software
- 7. Operating systems prefer hardware coherency they do not want the bug reports to the platform
- 8. Probe filters and directories will maintain power efficiency
- 9. Full coherency opens the doors to single source, native and managed code programming for heterogeneous platforms
- 10. Optimal architecture for heterogeneous computing on APUs and SOCs

#### **hUMA FEATURES**

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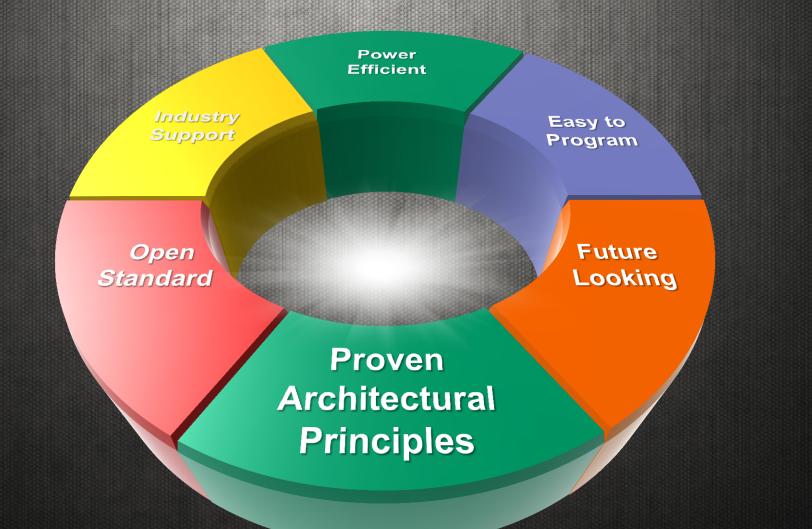


### hUMA BENEFITS



#### **BENEFITS OF HSA**

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#### **UNIFORM MEMORY BENEFITS TO DEVELOPERS**

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**EASE AND SIMPLICITY OF PROGRAMMING** Single, standard computing environments



#### **LOWER DEVELOPMENT COST**

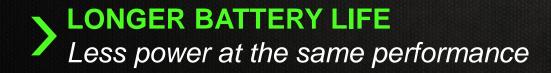
More efficient architecture enables less people to do the same work

#### **BENEFITS TO CONSUMERS**

**BETTER EXPERIENCES** Radically different user experiences

#### MORE PERFORMANCE

Getting more performance from the same form factor



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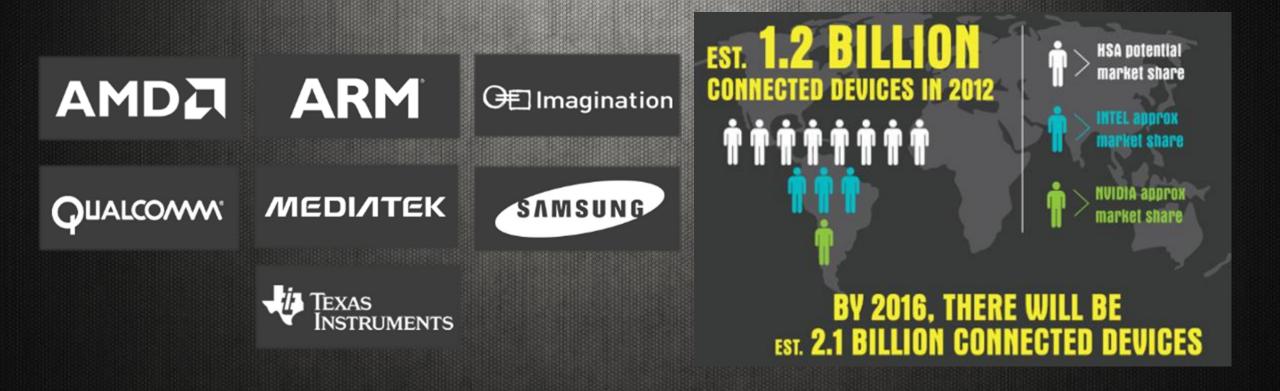




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#### SUPPORT FROM MAJOR INDUSTRY PLAYERS

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For more information go to: <u>http://hsafoundation.com/</u>

Source http://pinterest.com/pin/193021534001931884/

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THANK YOU

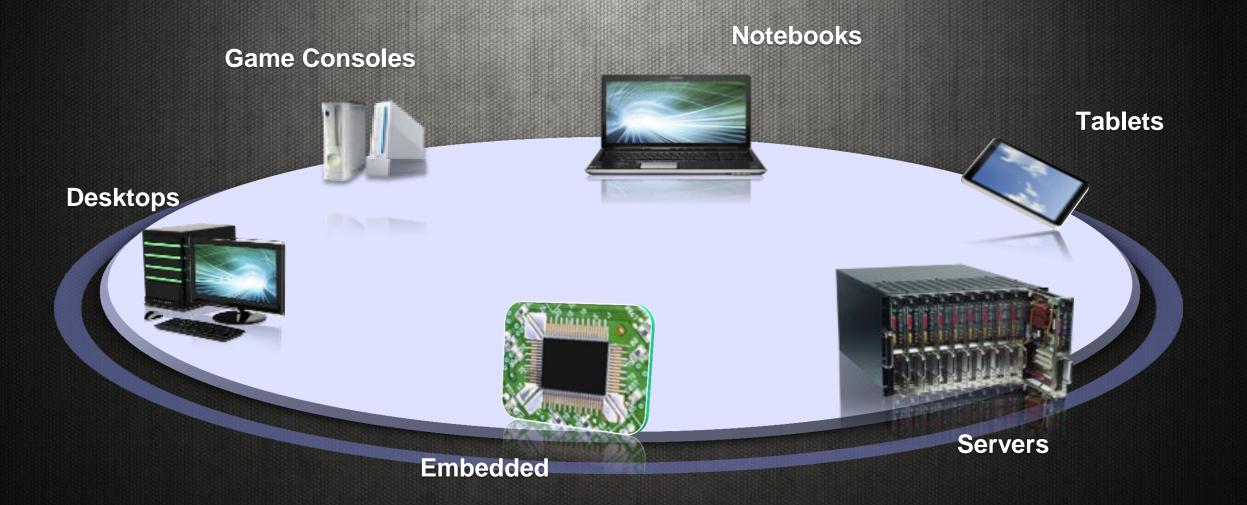
GFLOPS

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	CPU	CPU GFLOPS	GPU (RADEON)	<b>GPU GFLOPS</b>
> 2002	Pentium 4 (Northwood)	12.24	9700 Pro	31.2
> 2003	Pentium 4 (Northwood)	12.8	9800 XT	36.48
2004	Pentium 4 (Prescott	15.2	X850 XT	103.68
2005		15.2	X1800 XT	134.4
2006	Core 2 Duo	23.44	X1950	375
> 2007	Core 2 Quad	48	HD 2900 XT	473.6
> 2008	Q9650	96	HD 4870	1200
> 2009	Core i7 960	102.4	HD 5870	2720
> 2010	Core i7 970	153.6	HD 6970	2703
> 2011	Core i7 3960X	316.8	HD7970	3789
> 2012	Core i7 3970X	336	HD 7970 GHz Edition	4301

#### POTENTIAL MARKET IS HUGE





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